

App. Serial No. 10/539,383  
Docket No.: NL021429

**In the Drawings:**

Please find attached one Replacement Drawing Sheet for entry, Figures 2 and 3. The only change from the immediate version is in Figure 2 where the labels for current mirror 40 have been changed from T6' and T7' to T6 and T7 respectively. No other changes to the Figure 2 have been made.

Attachment: One Replacement Drawing Sheet.

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AUG 03 2007

**Remarks**

Claims 1-9 are pending in this application. Reconsideration and allowance of the application is respectfully requested.

The instant Office Action dated May 4, 2007, indicated objections to the drawings, that claims 1-3, 5, and 7-9 stand rejected under 35 U.S.C. § 102(e) over Prikhodko et al. (U.S. Pub. No. 2003/0087626); and that claims 4 and 6 are objected to as being dependent upon a rejected base claim.

Applicant appreciates Examiner's notation that claims 4 and 6 would be allowable if rewritten in independent form. In response, Applicant has incorporated the subject matter of claim 1 into claims 4 and 6. Thus, Applicant submits that claims 4 and 6 are in condition for allowance.

Regarding the objections to the drawings, Applicant has amended the Specification as indicated on page 2 of this paper to provide a specific reference to inductors L<sub>5</sub> and L<sub>6</sub>. Applicant notes that paragraph 0033 states that the third stage is substantially identical to the second stage 2, which is discussed in detail in paragraph 0032 including discussion of inductors L<sub>3</sub> and L<sub>4</sub> which correspond to inductors L<sub>5</sub> and L<sub>6</sub>. As such, no new matter has been added by this amendment. Applicant has also provided a replacement drawing sheet as indicated on page 3 of this response in which Figure 2 has been amended to change the labels for the transistors of current mirror 40 from T6' and T7' to T6 and T7 respectively. Accordingly, Applicant submits that the objections to the drawings have been overcome and Applicant requests that they be removed.

Applicant respectfully traverses the Section 102(e) rejection of claims 1-3, 5, and 7-9 because the cited portions of the Prikhodko reference do not correspond to claim limitations directed to providing a bias current to the control electrode of the signal amplification transistor. The Office Action asserts that Ibias corresponds to the claimed bias current; however, Prikhodko teaches that Ibias is provided to the collector of transistor 2 not to the control electrode of transistor 2. See, e.g., Figure 1 and Paragraph 0043. As such, the cited elements are not arranged as claimed as required by M.P.E.P. § 2131.

Moreover, the Prikhodko reference teaches that biasing circuit 6 provides a bias voltage to the base of transistor 2 (i.e., its control electrode). See, e.g., Figure 1 and

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Paragraphs 0036-0038. More specifically, Prikhodko's transistors 36 and 38 and current mirror (transistors 32, 34) provide a bias voltage to the base of transistor 2, as is further demonstrated by an identical arrangement in Applicant's Figure 2 consisting of current mirror 40' (transistors T6', T7') and transistors T<sub>14</sub> and T<sub>15</sub>, which provide a bias voltage instead of a bias current. See, e.g., Paragraph 0035 of Applicant's Specification. Thus, the cited portions of Prikhodko do not teach supplying a bias current to the control electrode of the signal amplification transistor as in the claimed invention. Accordingly, the Section 102(c) rejection of claims 1-3, 5, and 7-9 is improper and Applicant requests that it be withdrawn.

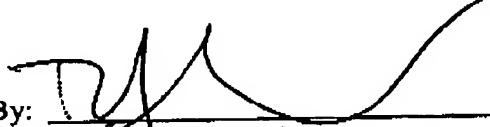
Applicant notes that all of the pending claims have been carefully reviewed and amended for the purpose of improving the language for presentation in a U.S. Letters Patent. None of these changes were necessary to overcome rejections concerning patentability in view of the above discussion. As examples, these changes include more careful delineation of the transition clause ("comprising" used only once in each claim), and reordering of the claim limitations with particular respect to the first stage and the first bias circuit in claim 1.

In view of the remarks above, Applicant believes that each of the rejections has been overcome and the application is in condition for allowance. Should there be any remaining issues that could be readily addressed over the telephone, the Examiner is asked to contact the agent overseeing the application file, Peter Zawilski, of NXP Corporation at (408) 474-9063 (or the undersigned).

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ANNOTATED SHEET

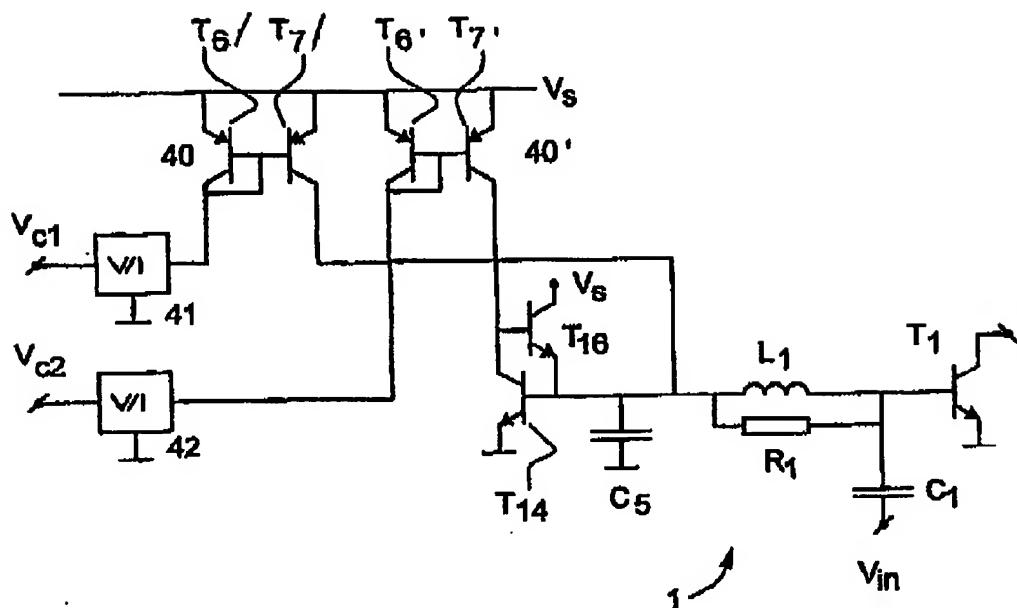


FIG. 2

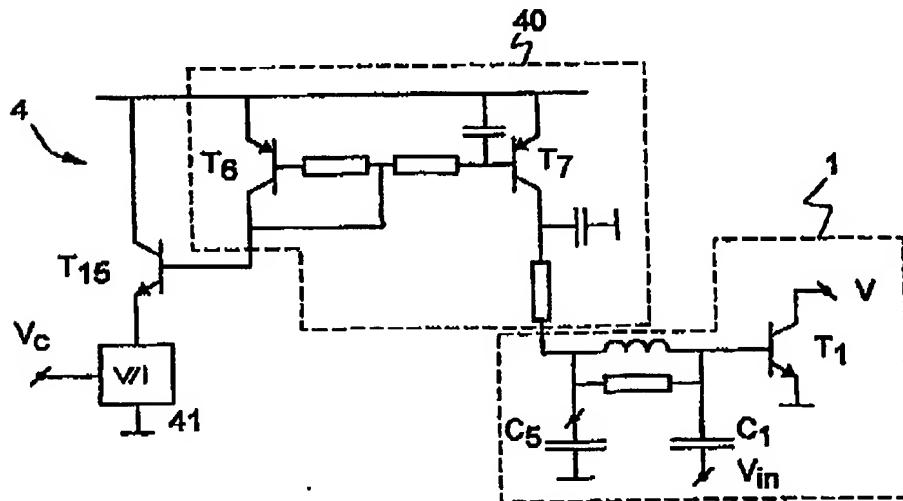


FIG. 3